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PY7-83262V

Electromagnetic Compatibility Test Report

For

**Sony Corporation
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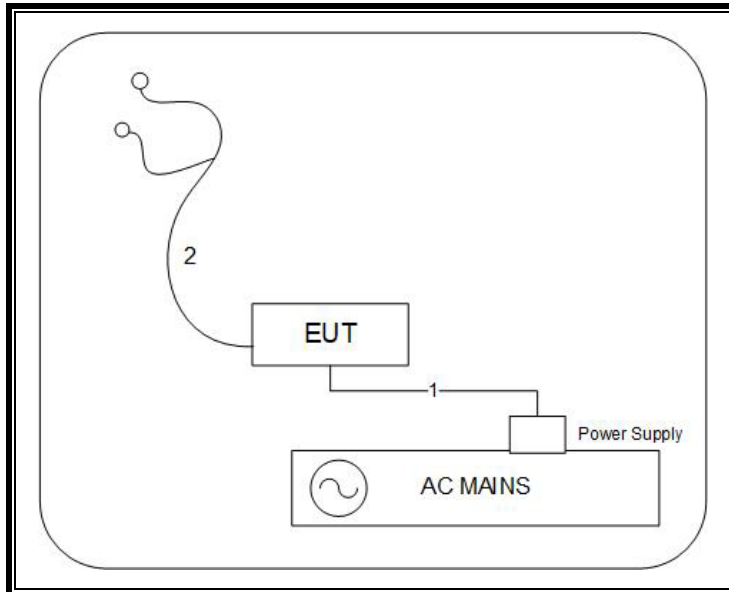
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REPORT REVISION HISTORY

Revision Date	Revision Version	Description	Revised By	Revision Reviewed By
2022-03-23	V1	Initial Issue	B. Kiewra	M. Antola

1.0 Block Diagram:

The diagram below illustrates the configuration of the equipment above.



2.0 SETUP PHOTOS

Photo of test setup for Mains Conducted Emissions

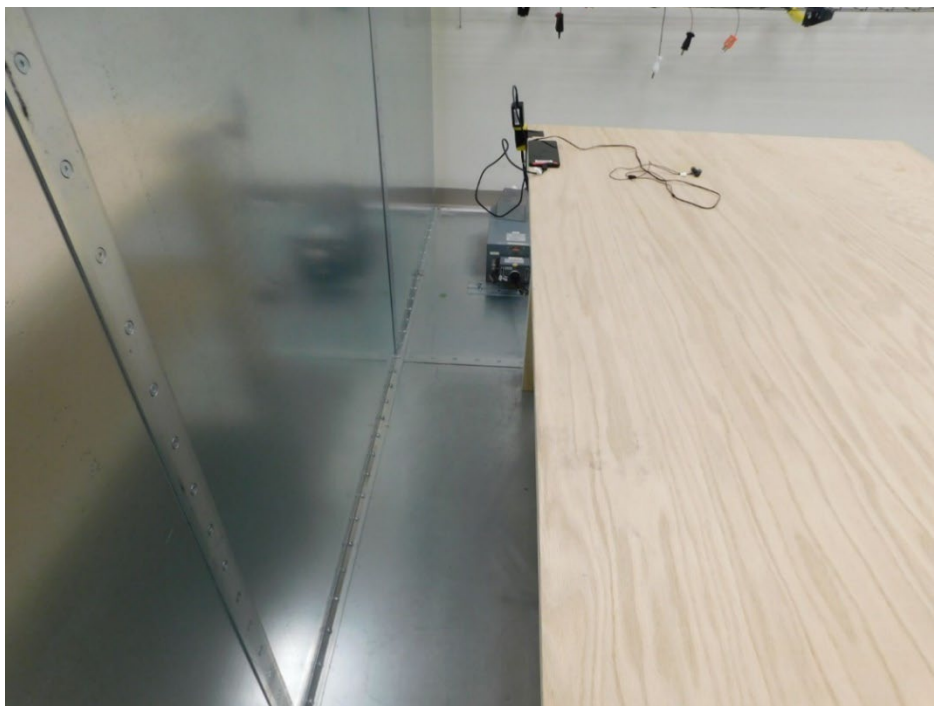
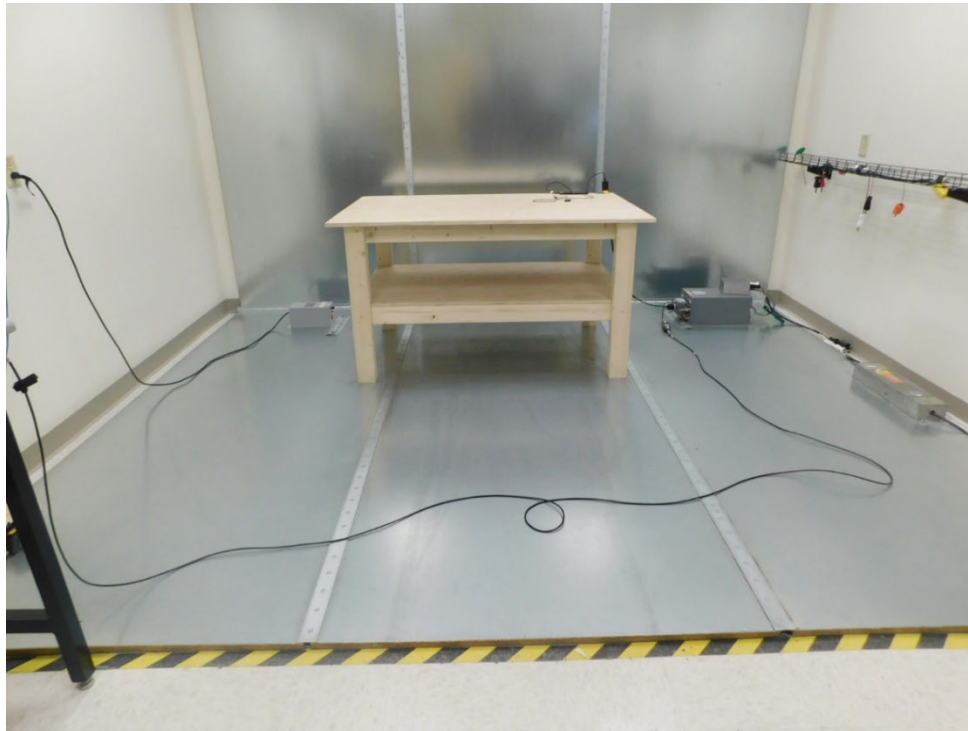


Photo of test setup for Radiated Emissions: 30-1000 MHz Battery

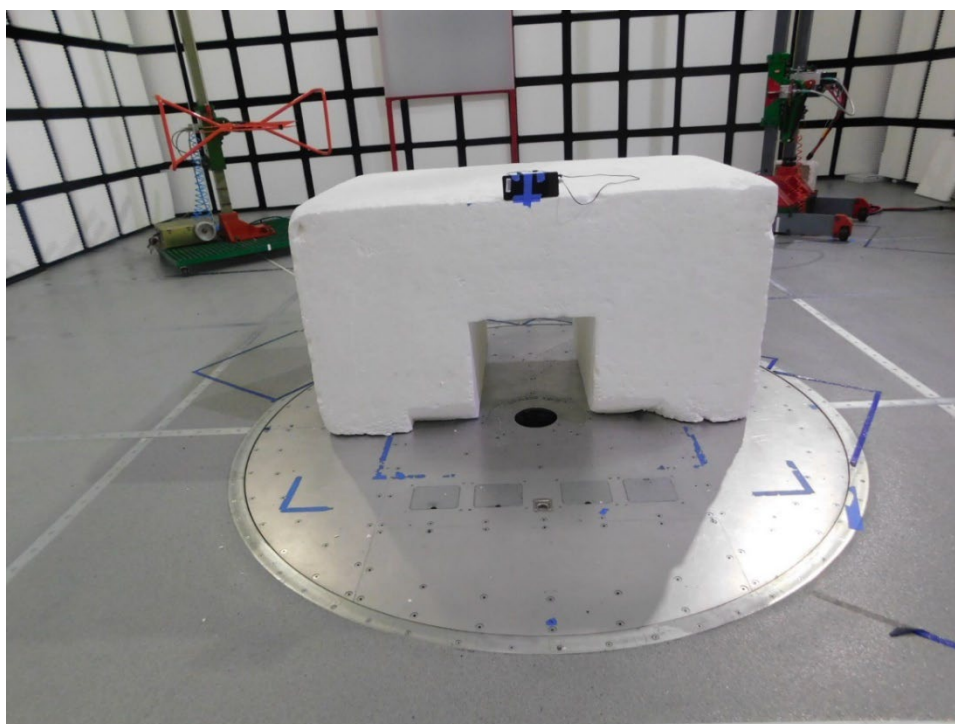


Photo of test setup for Radiated Emissions: >1 GHz Battery

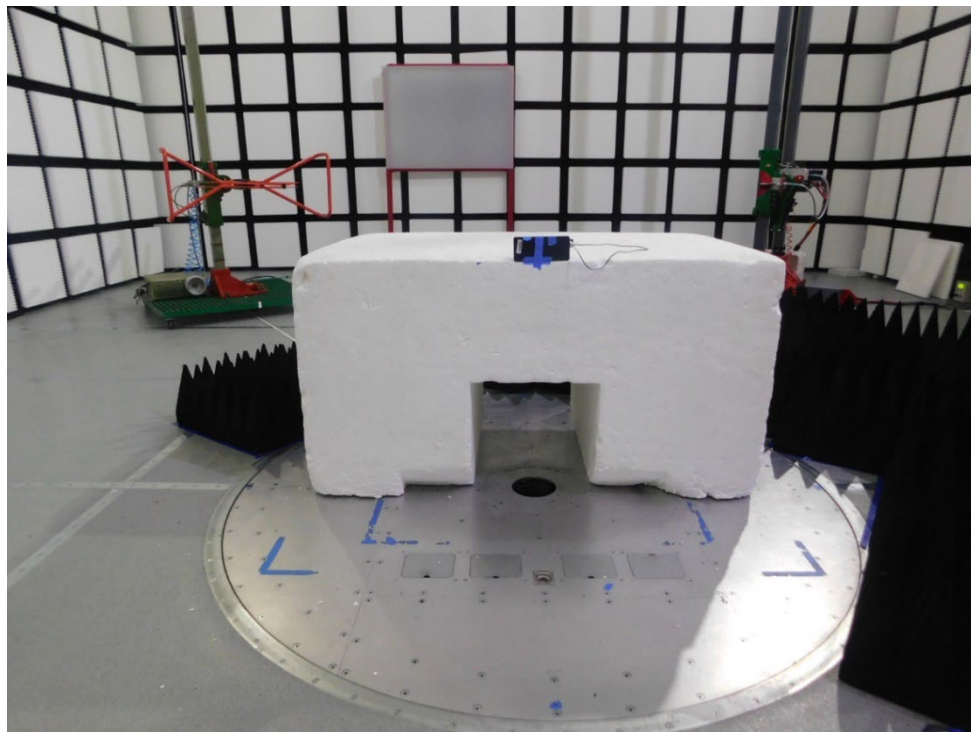


Photo of test setup for Radiated Emissions: 30-1000 MHz Power Supply, WWAN Rx

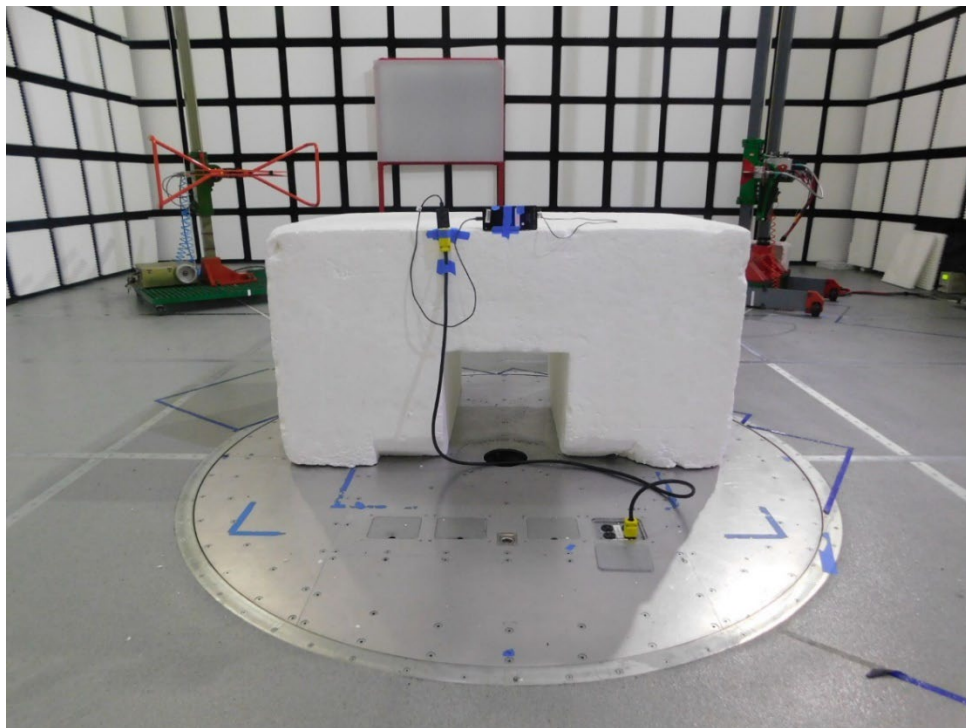
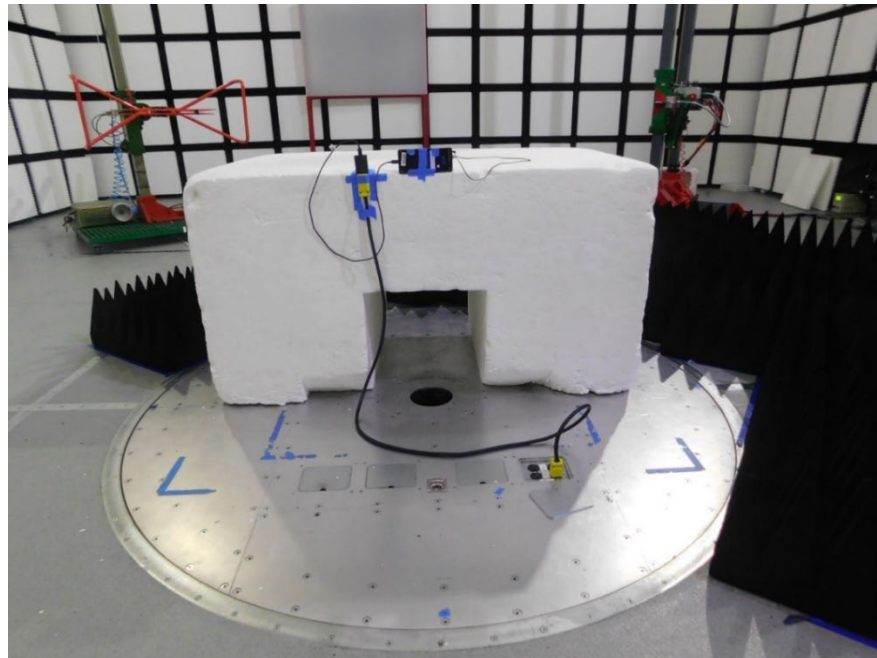


Photo of test setup for Radiated Emissions: >1 GHz Power Supply, WWAN Rx



END OF EXHIBIT