

November 25, 2013

Re: FCC ID: LDK102091

Applicant: Cisco Systems Inc

Date of Original E-mail: 11/25/2013

Please apply the "DFS Expedited Review process" for this device based on the following criteria:

Expedited Review Checklist	Previous FCC ID: LDK102087	New Application FCC ID: LDK102091
Technology:	802.11n / ac	802.11n / ac
Test labs used by this and previous report	Cisco Systems	Cisco Systems
Differences between the products such as TX power, modulation, receivers, processing circuitry, etc.	Supports 4 Tx /4 Rx paths, 17dBm Tx power per path	Supports 4 Tx / 4 Rx paths, 17dBm Tx power per path.
Differences / similarities in DFS functioning, circuitry, software, etc.	Uses Marvel chipset and Cisco Systems "Clean Air" algorithm for DFS radar detection. DFS detection is controlled completely by the Cisco SaGE engine (the Cisco IP pulse processing and detection engine) embedded in the Marvel chip.	Uses Marvel chipset and Cisco Systems "Clean Air" algorithm for DFS radar detection. DFS detection is controlled completely by the Cisco SaGE engine (the Cisco IP pulse processing and detection engine) embedded in the Marvel chip. This engine is identical to the SaGE engine used in LDK102087
Antenna information and differences for the minimum gain antennas	Supports 2-6 dBi antennas	Supports 2-6 dBi antennas
Bandwidth information and differences	Supports 20, 40 and 80MHz bandwidths	Supports 20,40 and 80MHz bandwidths

Jim Nicholson

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EMC Compliance Engineer

Best Regards,

Jim Nicholson Compliance Engineer

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